

38. (New) The level shift circuit according to claim 36, wherein the second transistor is turned off when stepping up a voltage of an input signal.

39. (New) The level shift circuit according to claim 36, wherein the second transistor limits a flow of current from the capacitor to the high potential power supply.

40. (New) The level shift circuit according to claim 1, wherein the limiting circuit includes a transistor.

41. (New) The level shift circuit according to claim 40, wherein the transistor is turned off when limiting the voltage provided to the capacitor.

42. (New) The level shift circuit according to claim 40, wherein the transistor limits a flow of current from the capacitor to the high potential power supply.

REMARKS

The Office Action dated March 25, 2002 has been received and carefully noted. The above amendments and the following remarks are submitted as a full and complete response thereto. Claims 36-42 are newly added. No new matter has been entered or amendments made that narrow the scope of any element of any claim. Applicants acknowledge the election of Group I, claims 1-10, to be further prosecuted. Claims 11-35 continue to be withdrawn from further consideration. Accordingly, claims 1-10 and 36-42 are pending in this application and are submitted for consideration.

Applicants acknowledge and thank the Examiner for indicating that claims 2-10 are allowable over the prior art.